Compliance status	Proposed text	Criticality	Parent heading
Compliant (STD)	Novatra Systems Technical Component	NA	
Compliant (STD)	Requirement Specifications:	NA	
Compliant (STD)	Overhead Access Module (OAM) Project Series: CX90, CX91	NA	
Not Applicable/Not Relevant	Doc Number: NST-2025-00024 NST-512873 These module requirement specifications	NA	1 Introduction (NST-467451)
Not Applicable/Not Relevant	(German KLH) define the technical and procedural NST-476455 For reasons of linguistic simplicity, references to	NA	1 Introduction (NST-467451)
Not Applicable/Not Relevant	individuals may appear in the masculine form. All references, NST-467447 Within this document, the term "shall" is to be	NA	1 Introduction (NST-467451)
Not Applicable/Not Relevant	interpreted as equivalent to "must". NST-467449 Together with the supporting documentation	NA	1.1 Document Landscape (NST-467449)
Not Applicable/Not Relevant	NST-467451 References to other documents are provided in	NA	1.1 Document Landscape (NST-467449)
Compliant (STD)	square brackets, e.g. [LHV 310 00x]. Such documents are listed NST-467453 This document is generated directly from the	NA	1.1.1 Document Creation (NST-467456)
Compliant (STD)	requirement management system maintained by Novatra NST-467454 Each Common Requirement is assigned a unique	NA	1.1.1 Document Creation (NST-467456)
Compliant (STD)	identifier (ID) for traceability. IDs may appear in the following	NA	1.1.1 Document Creation (NST-467456)
Compliant (STD)	• ID aligned left, text aligned right (requirement)	NA	1.1.1 Document Creation (NST-467456)
Compliant (STD)	• ID placed directly below the requirement text	NA	1.1.1 Document Creation (NST-467456)
Compliant (STD)	• ID shown in brackets after a heading	NA	1.1.1 Document Creation (NST-467456)
Compliant (STD)	NST-867453 The Integrator may also receive requirements	NA	1.1.1 Document Creation (NST-467456)
Compliant (STD)	through database export packages. NST-867916 Tagged text within a PDF export indicates	NA	1.1.1 Document Creation (NST-467456)
Clarification Required	classification metadata associated with the requirement ID. NST-467458 The document "Common Requirements	NA	1.1.2 Common Requirements (NST-467455)
Compliant (STD)	Pertaining to the Component Specifications" [LHV 310 00x] NST-467462 In case of discrepancies between this	NA	1.1.2 Common Requirements (NST-467455)
Compliant (STD)	specification and the Common Requirements [LHV 310 00x], NST-467457 Chapters or clauses in [LHV 310 00x] are	NA	1.1.2 Common Requirements (NST-467455)
Compliant (STD)	referenced by their requirement ID rather than chapter NST-467460 CRCS 3534713 In the Common Requirements	NA	1.1.2 Common Requirements (NST-467455)
Compliant (STD)	describes structural updates to the specification template. NST-467464 For each sourcing scope, the integrator receives a	NA	1.1.3 Logistics Component Requirement Specifications (NST-467465)
Compliant (STD)	corresponding Logistics Requirement Specification [LOG KLH] NST-867465 Upon receipt of the Logistics Requirements, the client's Purchasing	NA	1.1.3 Logistics Component Requirement Specifications (NST-467465)
Compliant (STD)	Department provides a logistics framework. This framework is	NA	Table 1: Test Parameters
Compliant (STD)	NST-0750936 The components described in this section are	NA	2.1.3 System Context (NST-467962)
Clarification Required	part of the Sesura Illumination Network (Chb) located within PV_LCH-43752 The LCH consists of a Prime Controller Node	3	2.1.4 Requirements for Components Involved 2.1.4.1 Component Contribution – Lumipara ControlHub (LCH)
Open	(PCN) and multiple Optic Flow Arrays (OFAs). The OFAs are PV_LCH-43768 The LCH receives control instructions through	NA	2.1.4 Requirements for Components Involved 2.1.4.1 Component Contribution – Lumipara ControlHub (LCH)
Compliant (STD)	the LNX Communication Layer (LNX-CL). PV_LCH-43719 Colors are transmitted as Trispectrum (RGB)	NA	2.1.4 Requirements for Components Involved 2.1.4.1 Component Contribution – Lumipara ControlHub (LCH)
Compliant (STD)	values. Each SLPU computes the required pulse-width	NA	2.1.4 Requirements for Components Involved 2.1.4.1 Component Contribution – Lumipara ControlHub (LCH)
Compliant (STD)	PV_LCH-43721 Target light values may be transmitted to the LCH and subsequently forwarded to the SLPUs. Execution can	NA	2.1.4.1.1 Component Tasks (PV_LCH-43746)
Compliant (STD)	PV_LCH-43720 The PCN must be able to control a configurable subset of LSEs within the OFAs via the assigned SLPUs.	NA	2.1.4.1.1 Component Tasks (PV_LCH-43746)
Clarification Required	PV_LCH-43759 Up to n SLPUs may be deployed. The number of PV_LCH-43730 The PCN transmits display commands to the	NA	2.1.4.1.1.1.2 Communication (PV_LCH-43707)
Compliant (STD)	relevant SLPUs. These are addressed according to the LNX-CL PV_LCH-43744 For reliability, identical user data may be	NA	2.1.4.1.1.1.2 Communication (PV_LCH-43707)
Compliant (STD)	cyclically resent (e.g., every 500 ms). If no new instructions are PV_LCH-43742 All required signals for LSEs, along	NA	2.1.4.1.1.1.2 Communication (PV_LCH-43707)
Compliant (STD)	with their expected behavior, are specified in the PV_LCH-43898 The LCH is addressed via the LNX-CL schedule,	NA	2.1.4.1.1.1.2 Communication (PV_LCH-43707)
Compliant (STD)	with each SLPU allocated a dedicated data set. PV_LCH-43728 The LCH must evaluate the LNX-CL schedule to	NA	2.1.4.1.1.1.2 Communication (PV_LCH-43707)
Compliant (STD)	determine which SLPU-specific data is relevant for execution. PV_LCH-43795 Up to 16 SLPUs can be addressed	NA	2.1.4.1.1.1.2 Communication (PV_LCH-43707)
Compliant (STD)	simultaneously. Each must be assigned an individual fixed PV_LCH-43763 This addressing is configured via the parameter	NA	2.1.4.1.1.1.2 Communication (PV_LCH-43707)
Open	PAIC_SLPU_n_address_[n]=[1...16]. (Requirement reserved for addressing-specific definitions,	4	2.1.4.1.1.1.3 Addressing (PV_LCH-43742)
Compliant (STD)	already aligned with LNX-CL addressing scheme) PV_LCH-43723 2.1.4.1.1.1.4 Control of Color	NA	2.1.4.1.1.1.3 Addressing (PV_LCH-43742)
Compliant (STD)	Color transmission occurs via the "Trispectrum color value" PV_LCH-43713 The LCH must convert the received Trispectrum	NA	2.1.4.1.1.1.3 Addressing (PV_LCH-43742)
Compliant (STD)	color value into the calibrated target color. PV_LCH-43762 Elementary color red shall be displayed at	NA	2.1.4.1.1.1.3 Addressing (PV_LCH-43742)
Compliant (STD)	values = 4095/0/0, activating only the red chip of the LSE. PV_LCH-43754 Elementary color green shall be displayed at	NA	2.1.4.1.1.1.3 Addressing (PV_LCH-43742)
Compliant (STD)	values = 0/4095/0. PV_LCH-43755 Elementary color blue shall PV_LCH-43753 If an IDLE value is received, the LCH must	NA	2.1.4.1.1.1.3 Addressing (PV_LCH-43742)
Compliant (STD)	disregard the corresponding command without interrupting PV_LCH-43754 If a color value falls outside the set of valid IDs	NA	2.1.4.1.1.1.3 Addressing (PV_LCH-43742)
Compliant (STD)	defined by the Client, the LCH must discard the command. 2.1.4.1.1.1.5 Brightness	2	2.1.4.1.1.1.3 Addressing (PV_LCH-43742)
Clarification Required	PV_LCH-43715 Brightness is controlled via the "Trispectrum brightness 43710 Brightness	3	2.1.4.1.1.1.6 Dimming Behavior (Interpolation) (PV_LCH-43719)
Compliant (STD)	PV_LCH-43745 The LCH shall perform linear interpolation between values transmitted by the PCN at 6.25 fps up to 100	NA	2.1.4.1.1.1.6 Dimming Behavior (Interpolation) (PV_LCH-43719)
Compliant (STD)	PV_LCH-43895 Interpolation can be enabled or disabled via the "Execution request" signal.	NA	2.1.4.1.1.1.6 Dimming Behavior (Interpolation) (PV_LCH-43719)
Compliant (STD)	PV_LCH-43743 Additional details on interpolation are provided in the technical lighting content section of the KLH.	NA	2.1.4.1.1.1.6 Dimming Behavior (Interpolation) (PV_LCH-43719)
Compliant (STD)	2.1.4.1.1.1.7 Memory Management (PV_LCH-43716) PV_LCH-43697 The LCH shall provide two distinct memory areas:	NA	2.1.4.1.1.1.6 Dimming Behavior (Interpolation) (PV_LCH-43719)
Compliant (STD)	1. Executing working memory	NA	2.1.4.1.1.1.6 Dimming Behavior (Interpolation) (PV_LCH-43719)
Compliant (STD)	2. Buffer memory	NA	2.1.4.1.1.1.6 Dimming Behavior (Interpolation) (PV_LCH-43719)
Compliant (STD)	PV_LCH-43766 Light images must be preloaded into the buffer	NA	2.1.4.1.1.1.6 Dimming Behavior (Interpolation) (PV_LCH-43719)
Compliant (STD)	memory independently of data in the executing memory. PV_LCH-43738 Commands in the executing memory shall be used	NA	2.1.4.1.1.1.6 Dimming Behavior (Interpolation) (PV_LCH-43719)
Compliant (STD)	to transfer preloaded images from the buffer into executing PV_LCH-51002 Different execution types are available,	NA	2.1.4.1.1.1.8 Execution Request (PV_LCH-51001)
Compliant (STD)	defining how the LCH processes incoming instructions. PV_LCH-51003 The execution type is transmitted via the	NA	2.1.4.1.1.1.8 Execution Request (PV_LCH-51001)
Compliant (STD)	"Execution request" signal. PV_LCH-51005 When set to IDLE, the addressed LCH shall	NA	2.1.4.1.1.1.8.1 IDLE (PV_LCH-51004)
Compliant (STD)	ignore all user data in the received request. Ongoing PV_LCH-51006 Note: The IDLE mode is primarily used for	NA	2.1.4.1.1.1.8.1 IDLE (PV_LCH-51004)
Compliant (STD)	synchronization of individual sequences. PV_LCH-51008 All SLPUs shall transfer data stored in buffer	NA	2.1.4.1.1.1.8.2 EXE (PV_LCH-51007)
Compliant (STD)	memory into working memory and begin interpolated PV_LCH-51010 All SLPUs shall transfer received data into	NA	2.1.4.1.1.1.8.3 LOAD (PV_LCH-51009)
Compliant (STD)	buffer memory when the value = LOAD. Ongoing operations <table border="1"><tr><td cellid="1524262" style="text-align:center" colspan="2"></td><td cellid="1524263"	NA	2.1.4.1.1.1.8.3 LOAD (PV_LCH-51009)
Compliant (STD)	2.1.4.1.1.1.8.4 NDEF & SNA (PV_LCH-51011) PV_LCH-51012 The LCH must interpret value range 2...3 as NDEF (Not	NA	2.1.4.1.1.1.8.3 LOAD (PV_LCH-51009)
Compliant (STD)	PV_LCH-51013 If the value = NDEF/SNA, the LCH shall ignore	NA	2.1.4.1.1.1.8.3 LOAD (PV_LCH-51009)
Compliant (STD)	or discard all information in the execution request. Ongoing 2.1.4.1.1.1.9 Initialization After Reset (PV_LCH-51014) PV_LCH-	NA	2.1.4.1.1.1.8.3 LOAD (PV_LCH-51009)
Compliant (STD)	4101 After a restart, the LCH shall initialize buffer memory	NA	2.1.4.1.1.1.8.3 LOAD (PV_LCH-51009)
Compliant (STD)	• Trispectrum Value = 0/0/0	NA	2.1.4.1.1.1.8.3 LOAD (PV_LCH-51009)
Compliant (STD)	• Execution type = IDLE	NA	2.1.4.1.1.1.8.3 LOAD (PV_LCH-51009)
Compliant (STD)	• Interpolation = OFF	NA	2.1.4.1.1.1.8.3 LOAD (PV_LCH-51009)
Compliant (STD)	PV_LCH-51017 The time since the last valid message must be	NA	2.1.4.1.1.1.10 Function Aborted Due to Timeout (PV_LCH-51016)
Non-Compliant	monitored. Each valid message resets the timer to 0. PV_LCH-51018 If the monitored time exceeds a configurable	5	2.1.4.1.1.1.10 Function Aborted Due to Timeout (PV_LCH-51016)
Compliant (STD)	parameter (0...3600s, with 0 = timer disabled), the current 2.1.4.1.2.1.1 Signal – Color Request (PV_LCH-51022)	NA	2.1.4.1.2.1.1 LNX-CL (PV_LCH-51021)
Compliant (STD)	PV_LCH-51023 Description: Defines color/brightness request	NA	2.1.4.1.2.1.1 LNX-CL (PV_LCH-51021)
Compliant (STD)	PV_LCH-51024 The corresponding 12-bit Trispectrum (R/G/B)	NA	2.1.4.1.2.1.1.2 Signal – Execution Request (PV_LCH-51025)
Compliant (STD)	PV_LCH-51026 Description: Defines the execution mode for	NA	2.1.4.1.2.1.1.2 Signal – Execution Request (PV_LCH-51025)
Compliant (STD)	the LCH. PV_LCH-51027 The execution request is transmitted	NA	2.1.4.1.2.2 Output Interface (PV_LCH-51028)
Compliant (STD)	PV_LCH-51029 The output interface is described in the KLH.	NA	2.1.4.1.2.2 Output Interface (PV_LCH-51028)
Compliant (STD)	PV_LCH-51031 See EE KLH of the LCH.	NA	2.1.4.1.3 Voltage Range / Current Values / Power Specifications (PV_LCH-51030)
Compliant (STD)	NST-51035 Each function description contains preconditions,	NA	2.1.5.1.1 Notes on the Following Function Descriptions (NST-51034)
Compliant (STD)	prerequisites, triggers, abort conditions, and end conditions. NST-51036 Precondition: A condition required before a	NA	2.1.5.1.1 Notes on the Following Function Descriptions (NST-51034)
Compliant (STD)	function is initiated. It may not remain valid throughout NST-51037 Functional prerequisite without triggering role: A	NA	2.1.5.1.1 Notes on the Following Function Descriptions (NST-51034)
Compliant (STD)	condition that must hold true throughout execution but NST-51038 Functional prerequisite with triggering role: A	NA	2.1.5.1.1 Notes on the Following Function Descriptions (NST-51034)
Compliant (STD)	prerequisite that, in combination with a trigger, activates the NST-51039 Functional trigger: An event that initiates the	NA	2.1.5.1.1 Notes on the Following Function Descriptions (NST-51034)
Compliant (STD)	function. • Flank-based: only on change (e.g., button press).	NA	2.1.5.1.1 Notes on the Following Function Descriptions (NST-51034)
Compliant (STD)	• Status-based: constant condition (e.g., manual switch ON).	NA	2.1.5.1.1 Notes on the Following Function Descriptions (NST-51034)
Compliant (STD)	NST-51040 Abortion condition: An explicit condition that	NA	2.1.5.1.1 Notes on the Following Function Descriptions (NST-51034)
Compliant (STD)	aborts execution immediately (e.g., opposite command input). NST-51041 End condition: When fulfilled, the function	NA	2.1.5.1.1 Notes on the Following Function Descriptions (NST-51034)
Compliant (STD)	terminates as planned (e.g., end position reached). NST-51042 Logical dependencies between conditions must be indicated (AND / OR).	NA	2.1.5.1.1 Notes on the Following Function Descriptions (NST-51034)

Compliant (STD)	NST-51044 The component shall support multiple power states:	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	1. Sleep Mode – All peripheral elements off, current consumption within sleep limits.	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	2. On Mode – Component fully active; periphery powered, communication active, light sources enabled on request.	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	3. Power Safe Mode – Activated if brightness = 0 for a duration defined in PLCH_PowerSafe_[t]. Non-essential submodules are	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	a. Recovery to active mode must occur in < 100 ms.	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	b. Communication remains active unless LNX-CL enters sleep.	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	c. If LNX-CL sleep occurs without timeout, the component also enters Sleep Mode.	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	d. If LNX-CL timeout occurs, all periphery must shut down and enter Sleep	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	Mode.	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	e. Diagnostic mode entry from Power Safe Mode must be possible.	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	4. Communication Off Mode – If brightness > 0 while LNX-CL is	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	a. A timer starts when LNX-CL enters sleep.	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	b. If no valid LNX-CL message is received before PLCH_CoMtimeout_[t] expires, the system enters Sleep Mode.	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	c. Every valid message resets the timer (including "idle" messages).	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	d. Parameter PLCH_CoMtimeout_[t] is distinct from the standard LNX-CL timeout.	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	e. Diagnostic mode entry from Communication Off Mode must be possible.	NA	2.1.5.1.2 Power Status Management (NST-51043)
Compliant (STD)	NST-52002 The Integrator shall provide a valid Supplier-ID to the Client. If such an ID is not available, the Integrator shall	NA	2.1.5.1.3 LNX-CL Performance (NST-52001)
Compliant (STD)	NST-52003 The component shall support a baudrate of 19.2 Kbaud.	NA	2.1.5.1.3 LNX-CL Performance (NST-52001)
Compliant (STD)	NST-52004 Note: Timing intervals are defined to ensure that user actions are executed without perceptible delay.	NA	2.1.5.1.3 LNX-CL Performance (NST-52001)
Compliant (STD)	NST-52005 The component shall be capable of waking up within 200 ms from Sleep Mode to perform a requested	NA	2.1.5.1.3 LNX-CL Performance (NST-52001)
Compliant (STD)	NST-52006 At the latest, the second Prime Controller Node (PCN) request in the functional schedule must be evaluated	NA	2.1.5.1.3 LNX-CL Performance (NST-52001)
Compliant (STD)	NST-52008 The component shall implement the LNX-CL signal interface in compliance with the current LDF (LNX description	NA	2.1.5.1.4 LNX-CL Signals (NST-52007)
Compliant (STD)	NST-52010 During initial startup, each Photon Driver (PD) shall store its assigned address in non-volatile memory.	NA	2.1.5.1.5 Photon Driver Initialization (NST-52009)
Compliant (STD)	NST-52011 If the selected PD does not support non-volatile storage (e.g., state machine limitations), the following	NA	2.1.5.1.5 Photon Driver Initialization (NST-52009)
Compliant (STD)	NST-52012 Each time the component powers on (from Sleep, Power Safe Mode, or after interruptions), all PD addresses	NA	2.1.5.1.5 Photon Driver Initialization (NST-52009)
Compliant (STD)	NST-52013 During initialization, the internal master logic shall ensure that all PDs are dropped, addressed and synchronized.	NA	2.1.5.1.5 Photon Driver Initialization (NST-52009)
Compliant (STD)	NST-52014 The expected number of PDs or communication slaves per OFA shall be derived from coding parameters and	NA	2.1.5.1.5 Photon Driver Initialization (NST-52009)
Compliant (STD)	NST-52015 If the detected PDs deviate from the expected count, the system shall broadcast a configuration error on the	NA	2.1.5.1.5 Photon Driver Initialization (NST-52009)
Compliant (STD)	NST-52016 No TSpectrum Emitters (TSEs) shall activate during initialization, and no visual feedback is permitted.	NA	2.1.5.1.5 Photon Driver Initialization (NST-52009)
Compliant (STD)	NST-52017 The Integrator shall design the electronic circuitry to prevent voltage dips at the PD power supply that could	NA	2.1.5.1.5 Photon Driver Initialization (NST-52009)
Compliant (STD)	NST-52019 This function is triggered via the diagnostic interface. NST-52020 It shall be implemented as the DiagExec	NA	2.1.5.1.5.1 Initialization Routine (NST-52018)
Compliant (STD)	NST-52021 This function shall run as a synchronous routine, since execution	NA	2.1.5.1.5.1 Initialization Routine (NST-52018)
Compliant (STD)	NST-52022 The routine shall only run if explicitly invoked via the diagnostic interface.	NA	2.1.5.1.5.1 Initialization Routine (NST-52018)
Compliant (STD)	NST-52023 If regular addressing is required, visual feedback shall only be provided when the diagnostic service is externally	NA	2.1.5.1.5.1 Initialization Routine (NST-52018)
Compliant (STD)	NST-52025 An extended diagnostic session must be active.	NA	2.1.5.1.5.1.1 Precondition (NST-52024)
Compliant (STD)	NST-52027 The routine shall be invoked through the diagnostic interface using the corresponding execution command.	NA	2.1.5.1.5.1.2 Function Trigger (NST-52026)
Compliant (STD)	NST-52029 The PCN shall readdress all internal slaves or Photon Drivers on both left and right OFAs via the internal bus	NA	2.1.5.1.5.1.3 Function Execution (NST-52028)
Compliant (STD)	NST-52030 The monitoring functions must remain active during the routine.	NA	2.1.5.1.5.1.3 Function Execution (NST-52028)
Compliant (STD)	NST-52032 The function shall be aborted if a stopRoutine command is received via the diagnostic interface.	NA	2.1.5.1.5.1.4 Abortion Condition (NST-52031)
Compliant (STD)	NST-52034 The function execution shall conclude normally.	NA	2.1.5.1.5.1.5 End Condition (NST-52033)
Compliant (STD)	NST-52035 If monitoring concludes with a fault, the respective failure signal shall be transmitted.	NA	2.1.5.1.5.1.5 End Condition (NST-52033)
Compliant (STD)	NST-52036 After completing the initialization routine, the component shall provide visual feedback through the TSEs:	NA	2.1.5.1.5.1.5 End Condition (NST-52033)
Compliant (STD)	• If successful: all TSEs illuminate GREEN at full brightness for 10 seconds.	NA	2.1.5.1.5.1.5 End Condition (NST-52033)
Compliant (STD)	• If a fault occurs: all TSEs blink RED at full brightness, 0.5 Hz for 10 seconds.	NA	2.1.5.1.5.1.5 End Condition (NST-52033)
Compliant (STD)	• If partial operation only is possible, the maximum available TSEs shall activate.	NA	2.1.5.1.5.1.5 End Condition (NST-52033)
Compliant (STD)	NST-52037 The return value shall be sent when the addressing routine completes, though visual feedback may continue.	NA	2.1.5.1.5.1.5 End Condition (NST-52033)
Open	NST-52038 Definitions of the TSpectrum colors (GREEN, RED, BLUE) are described in Chapter 10229574: Lighting Function,	NA	2.1.5.1.5.1.5 End Condition (NST-52033)
Open	NST-52042 All requirements for TSpectrum Emitters (TSEs) and their RGB values are valid only if TSEs are installed in the	NA	2.1.5.1.6.1.1 General Light Source Requirements (NST-52041)
Compliant (STD)	NST-52044 The positioning and design of the component, including its housing, must be coordinated with the client.	NA	2.1.5.1.6.1.2 Assembly Integration Requirements (NST-52043)
Open	NST-52045 The integrator must align material and color properties of surrounding components (affecting optical	NA	2.1.5.1.6.1.2 Assembly Integration Requirements (NST-52043)
Compliant (STD)	NST-52046 Any surrounding materials affecting visibility must be coordinated with the development authority prior to blank	NA	2.1.5.1.6.1.2 Assembly Integration Requirements (NST-52043)
Compliant (STD)	NST-52047 All interfaces shall be validated. NST-52048 Carry-over interfaces must be validated for each release cycle.	NA	2.1.5.1.6.1.2 Assembly Integration Requirements (NST-52043)
Compliant (STD)	NST-52049 Design and development implementation must be coordinated with all adjacent modules.	NA	2.1.5.1.6.1.2 Assembly Integration Requirements (NST-52043)
Compliant (STD)	NST-52050 In the client's Cooperation Kickoff Meeting, milestones (e.g., blank or tool release) shall be coordinated to	NA	2.1.5.1.6.1.2 Assembly Integration Requirements (NST-52043)
Compliant (STD)	NST-52052 Light sources must comply with AEC-Q automotive certification.	NA	2.1.5.1.6.1.3 Light Source Requirements (NST-52051)
Compliant (STD)	NST-52053 Final light source selection (including binning) must be approved by the Client's development team.	NA	2.1.5.1.6.1.3 Light Source Requirements (NST-52051)
Compliant (STD)	NST-52054 Light source datasheet limits must not be exceeded. Real-world boundary conditions shall be validated	NA	2.1.5.1.6.1.3 Light Source Requirements (NST-52051)
Compliant (STD)	NST-52055 Light sources shall not emit radiation in the ultraviolet range.	NA	2.1.5.1.6.1.3 Light Source Requirements (NST-52051)
Open	NST-52056 Light sources must be safeguarded against excessive self-heating and external heating.	NA	2.1.5.1.6.1.3 Light Source Requirements (NST-52051)
Open	NST-52057 The design must allow replacement of a light source with an equivalent from another manufacturer (form	NA	2.1.5.1.6.1.3 Light Source Requirements (NST-52051)
Open	NST-52058 Second source LEDs must be identified and validated without further modification.	NA	2.1.5.1.6.1.3 Light Source Requirements (NST-52051)
Compliant (STD)	NST-52059 Production processes (e.g., pick-and-place vacuum pipettes) must be suitable for TSEs and approved by the	NA	2.1.5.1.6.1.3 Light Source Requirements (NST-52051)
Compliant (STD)	NST-52060 Lighting concepts shall prioritize designs that avoid additional calibration (brightness or color) during	NA	2.1.5.1.6.1.3 Light Source Requirements (NST-52051)
Compliant (STD)	2.1.5.1.6.1.3.1 Detect and Mitigate Single or Multi-Emitter Light Sources (NST-6359121) NST-6359122 Any defect occurring in	NA	2.1.5.1.6.1.3 Light Source Requirements (NST-52051)
Compliant (STD)	NST-6359125 If a single emitter within a TSpectrum Emitter (TSE) or pixel is defective, this failure must be detected.	NA	2.1.5.1.6.1.3 Light Source Requirements (NST-52051)
Compliant (STD)	NST-6359124 Following detection, the affected multi-emitter TSE or pixel shall be automatically deactivated.	NA	2.1.5.1.6.1.3 Light Source Requirements (NST-52051)
Compliant (STD)	NST-6359123 Strategies for handling multiple defects shall be coordinated with the client (Novatra Systems).	NA	2.1.5.1.6.1.3 Light Source Requirements (NST-52051)
Compliant (STD)	<table border="1" ><tr><th id="1524213" style="text-alignangnter;">Temperature (Tasse (ths 4th cellids:1524214ll	NA	Table 2: Temperature
Compliant (STD)	NST-6359131 The Integrator shall conduct the market-specific launch-overs with Novatra Systems to align precisely with	NA	2.2 General Specifications (NST-6359130)
Compliant (STD)	NST-6359132 The Integrator should deliver a new compiled software binary with every development loop.	NA	2.2 General Specifications (NST-6359130)
Non-Compliant	NST-6359133 If the Integrator deems it essential for project performance, it may appoint a resident engineer to facilitate	3	2.2 General Specifications (NST-6359130)
Non-Compliant	NST-6359141 The Unified Cybersecurity Terms (UCT) shall be complied with. For execution of the assignment, diagnosis of	NA	2.3 Security and Protection (NST-6359140)
Compliant (STD)	NST-6359142 If the Integrator does not operate proprietary software for SAC management, the KeyMesh Manager (KMM)	NA	2.3 Security and Protection (NST-6359140)
Open	• UCT for Secure Access Credentials [MSS 10916]	NA	2.3 Security and Protection (NST-6359140)
Open	• UCT for Key Mesh Manager [MSS 10917]	NA	2.3 Security and Protection (NST-6359140)
Open	NST-6359142 If the Integrator does not operate proprietary software for SAC management, the KeyMesh Manager (KMM)	NA	2.3 Security and Protection (NST-6359140)
Open	NST-6359143 Both SAC and KMM are governed under the UCT framework, consisting of the following governing documents:	NA	2.3 Security and Protection (NST-6359140)
Open	• UCT for Secure Access Credentials [MSS 10916]	NA	2.3 Security and Protection (NST-6359140)
Open	• UCT for Key Mesh Manager [MSS 10917]	NA	2.3 Security and Protection (NST-6359140)